

Fig 1

Prior art

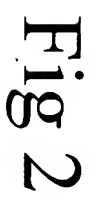


Fig 2

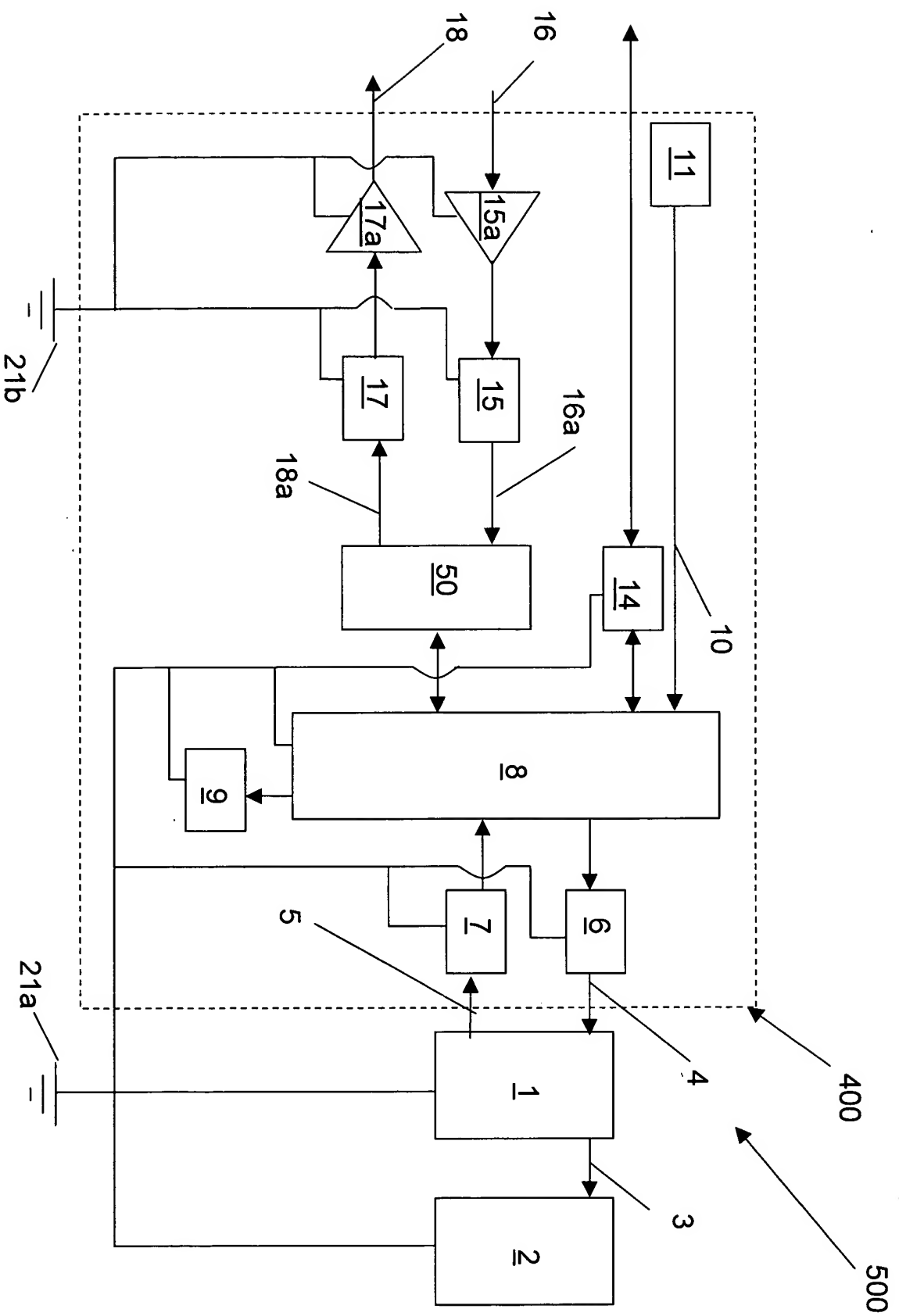


Fig 3

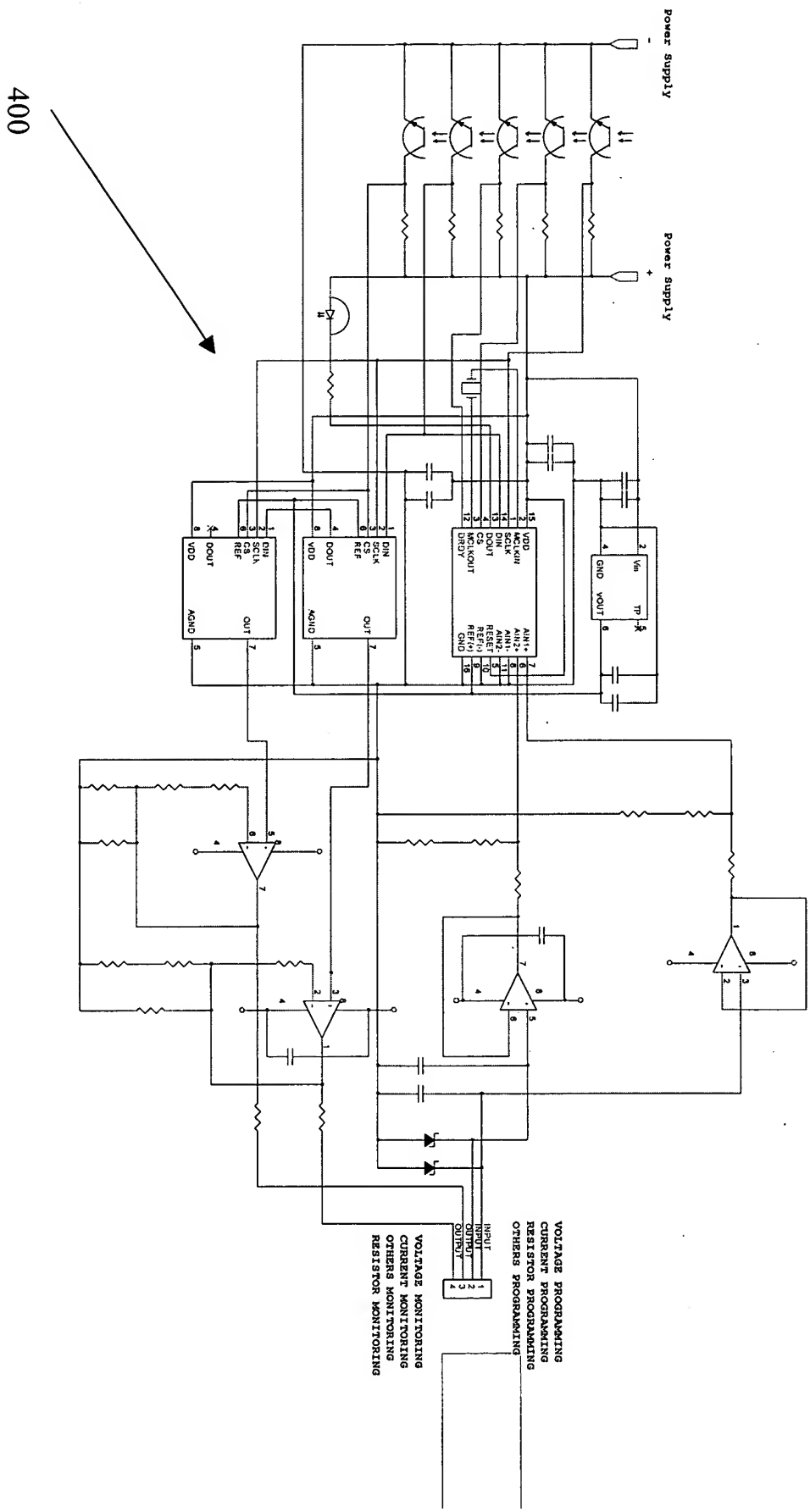
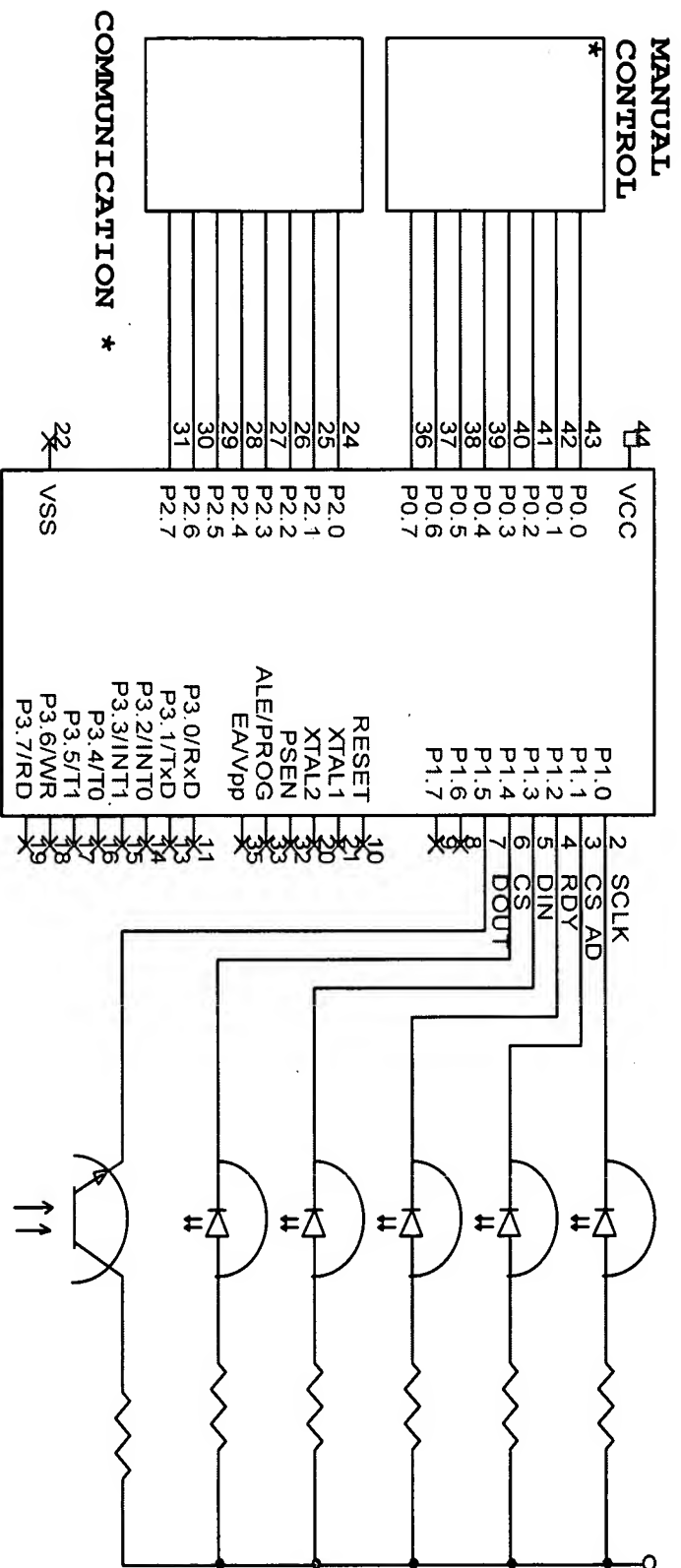


Fig 4a



**\*Programming of input and output signals levels.**

**Fig 4b**

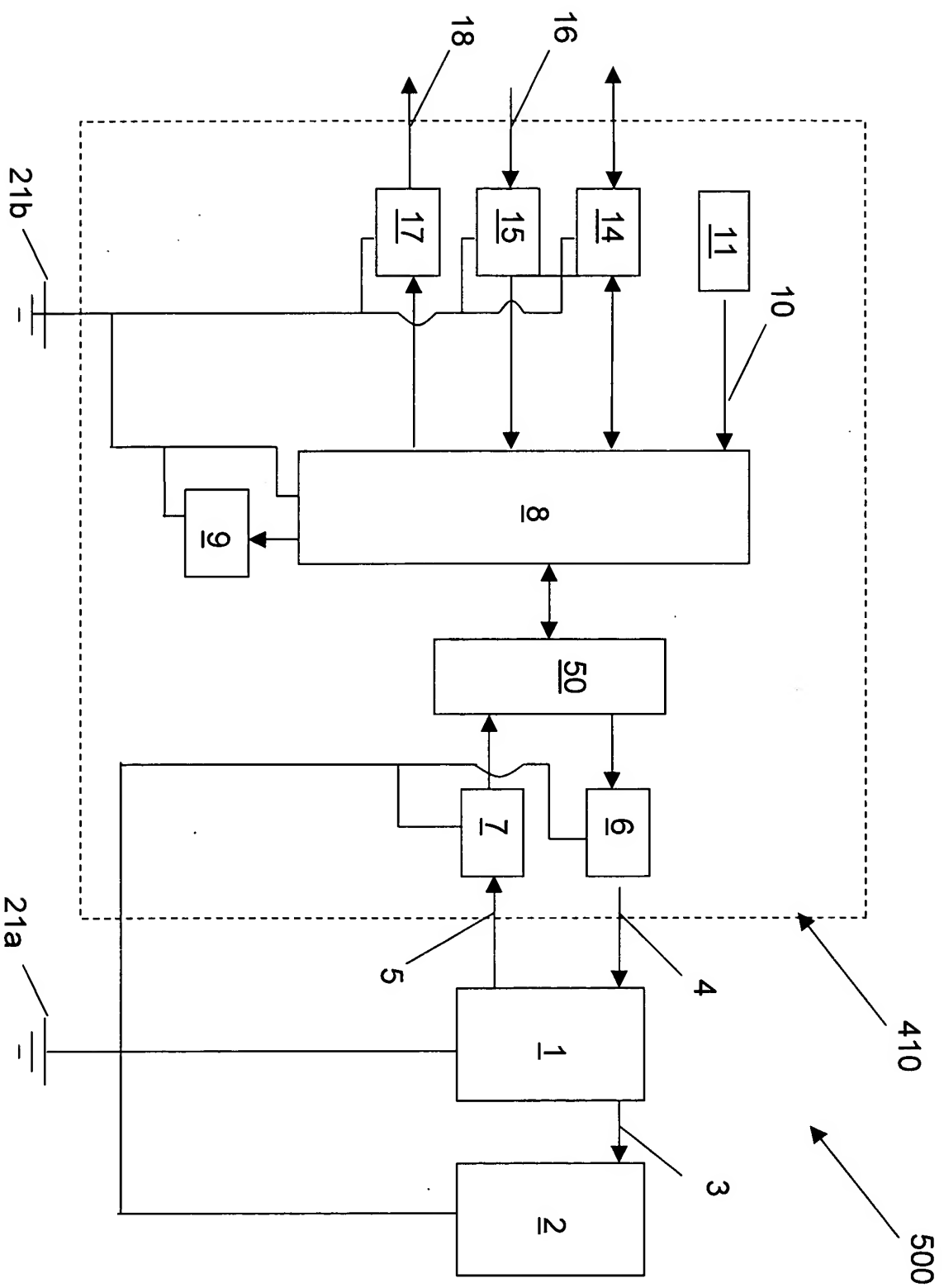


Fig 5